



J1 goes to 4-aa holder with cover and internal switch. Nominal +5V input.

NOT LOADED

DDS PHASE ACCUMULATOR

TTL
CARRY

OUT
L2
1.2uH
C12 270p
LPF
C11 33p
TRI

R10 sets AMP
R10 50
J2
HDR2

6-bit DAC Triangle Out

Z1 390sip
Z2 3.3ksip

OSCILLATOR

U6D 74AC86
R1 15p
R1 is replaced with 15p cap.
Adjust C1 for 1.0MHz with setting of 200 (0c8 hex).
C1 50p ORN
C2 33p

rad_hf.sch

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Proprietary Information

Title Simple 12-bit DPS, 5kHz resolution.

Size Document Number RDDSHF-1001

REV -

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C5 and C6 values are not critical. Values included with kit may vary. Your kit may have 74F283 or 74ACT283 depending upon supplies. If 74ACT283, then U7=74AC86.